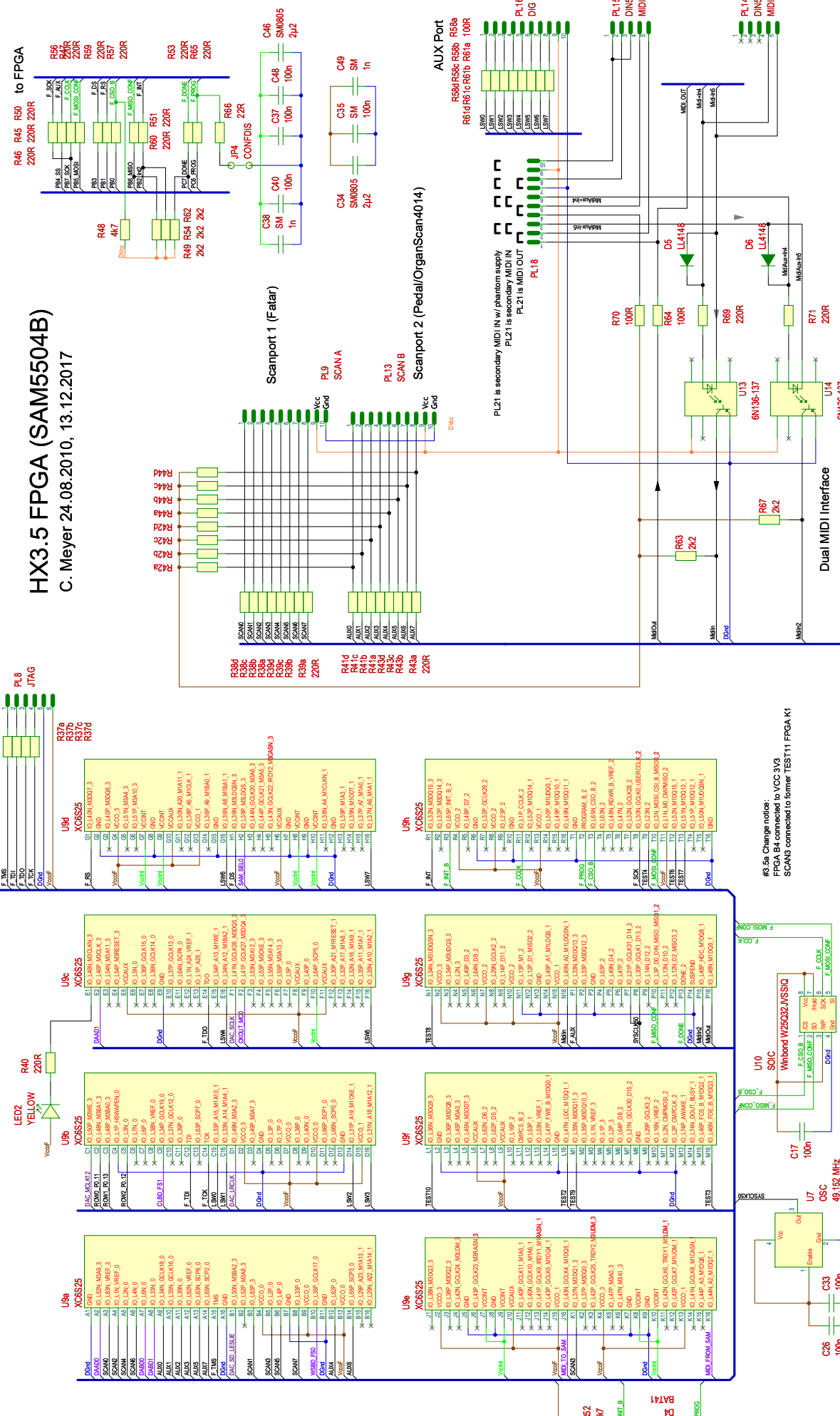


HX3.5 FPGA (SAM5504B)

C. Meyer 24.08.2010, 13.12.2017



#5.5a Change notes:
 VCC3V3 FPGA B4 connected to VCC3V3
 SCAN3 connected to former TEST11: FPGA K1